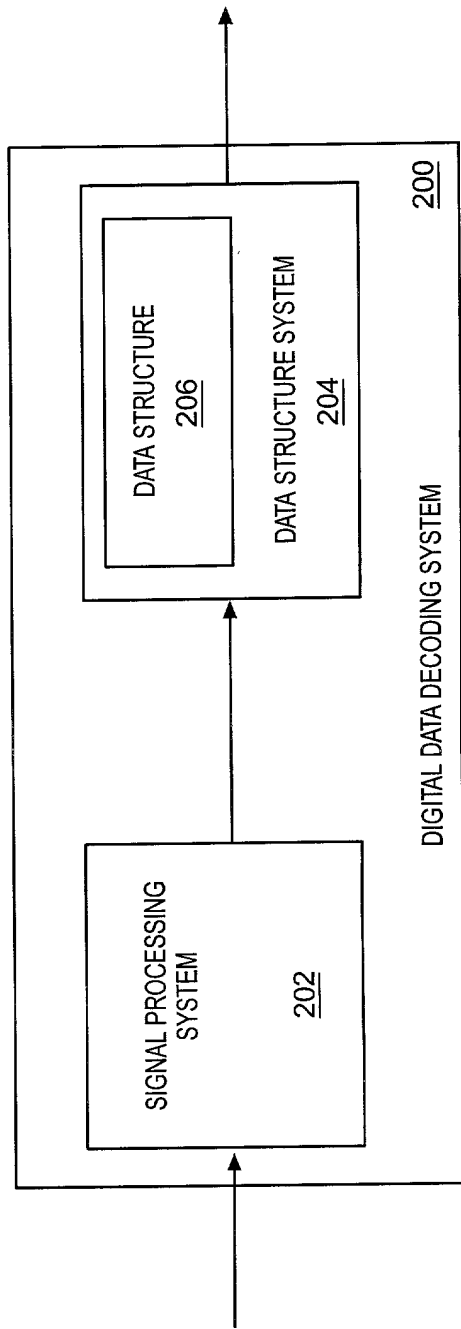


**FIG. 1**



**FIG. 2**

FIG. 3

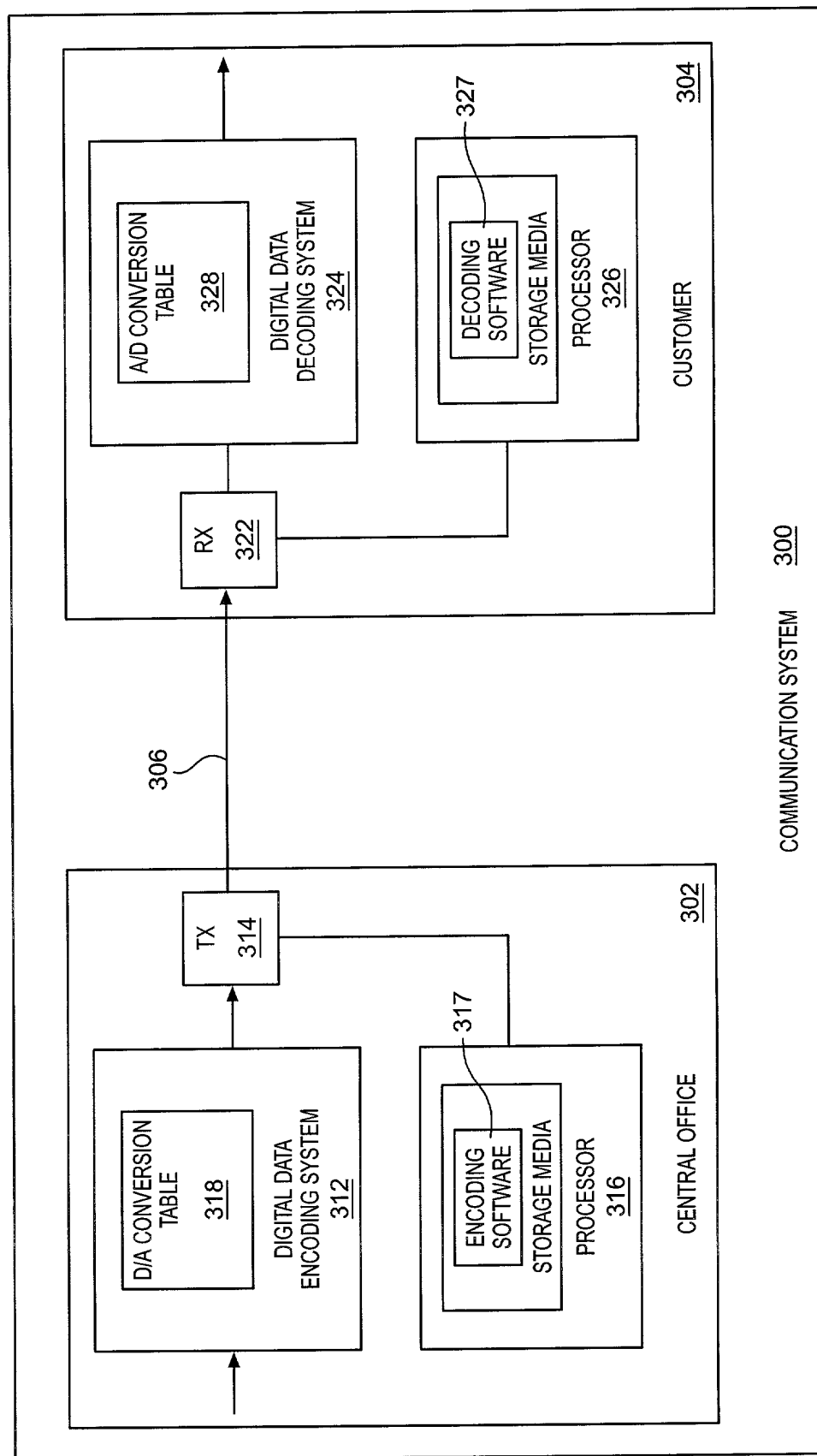


FIG. 4

BITS	0000	0001	0010	0011	0100	...	1010	1011	1100	...
0000		+1, -1	+1, -2	+1, -3	+1, -4	...	+5, -2		+3, -4	
0001	+8, -2		+9, -2		+5, -4	...	+13, -2	+4, -3	+7, -4	
0010	+8, -4		+17, -2		+9, -4	...	+7, -6		+11, -4	
0011						...				
0100						...				
:	:	:	:	:	:	...	:	:	:	
1010						...				
1011						...				
1100						...				
:										

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AMPLITUDE (V)	-1	-2	-3	-4	-5	-6	-7	-8	-9	...
+1	0000 0001	0000 0010 0011	0000 0011	0000 1100 0100						
+2										
+3				0000 1100						
+4			0001 1011							
+5		0000 1010		0001 0100						
+6										
+7				0001 1100		0010 1010				
+8		0001 0000		0010 0000						
+9		0001 0010		0010 0100						
...										

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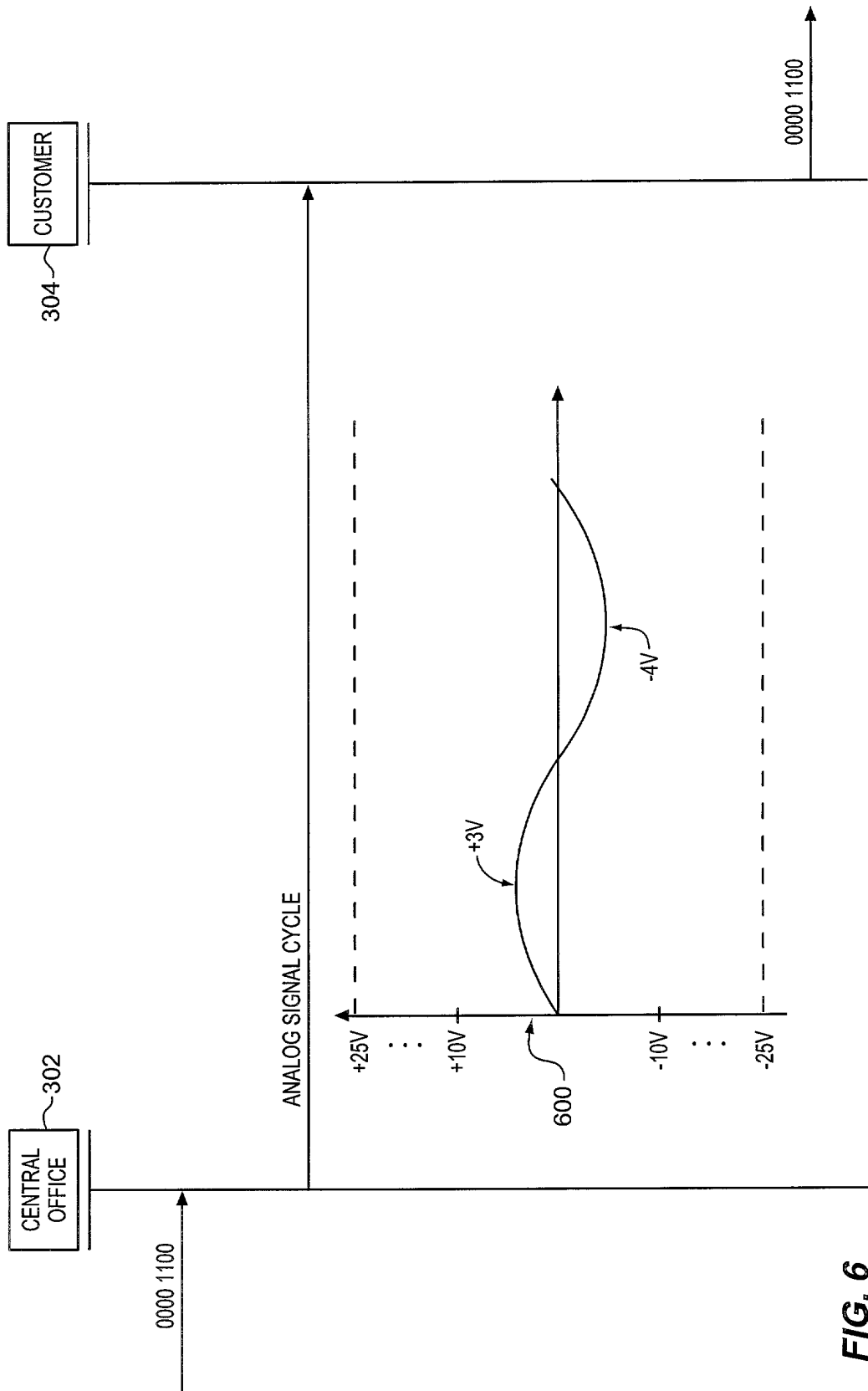
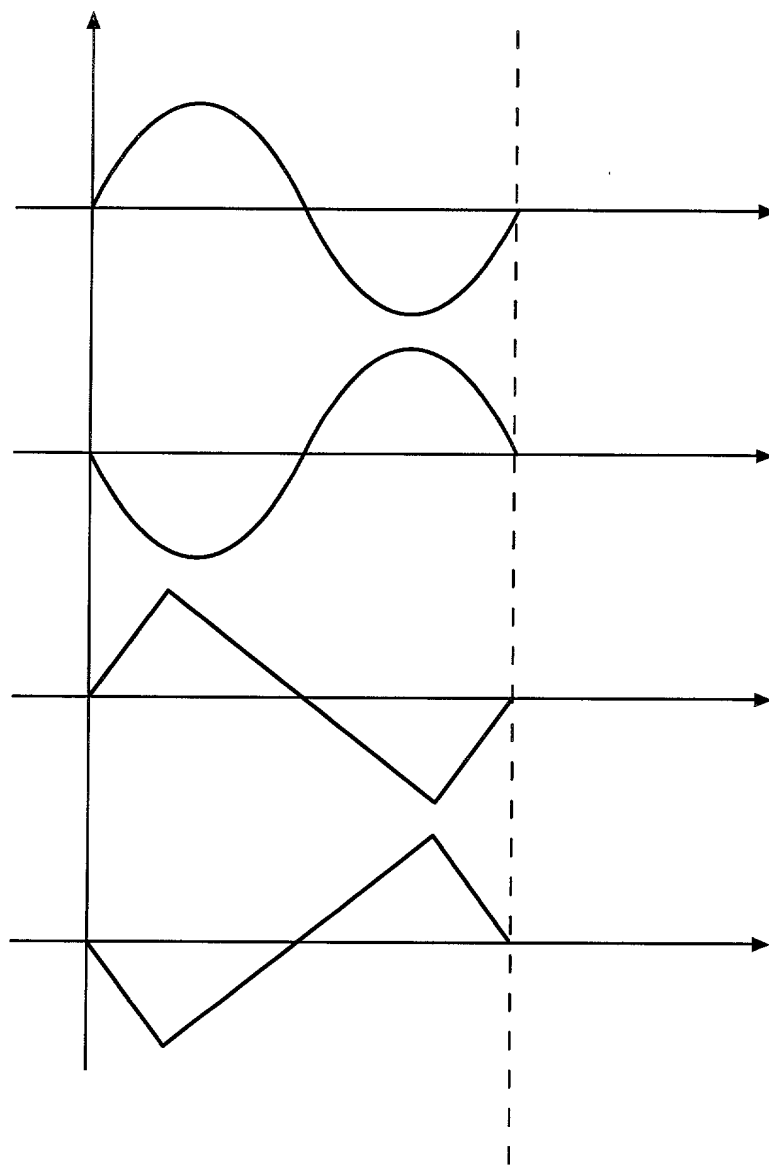


FIG. 6



**FIG. 7**